

Amendment to the Specification

Please replace the paragraph beginning on page 3, line 25, with the following:

Referring to Fig. 2B, each of the sensors **205** preferably include a seismic sensor **235** and an application specific integrated circuit ("ASIC") **240**. The design and operation of the seismic sensor **235** and the ASIC **240** are preferably substantially as disclosed in the following co-pending U.S. Patent Applications Serial No. [[_____]] 09/936,640, Attorney Docket No. 14737.737, filed on [[_____]] September 12, 2001, Serial No. [[_____]] 09/936,634, Attorney Docket No. 14737.739, filed on [[_____]] September 12, 2001, and Serial No. 08/935,093, Attorney Docket No. IOS011, filed on September 25, 1997, the contents of which are incorporated herein by reference. The ASIC **240** preferably further includes a local non-volatile memory **245**. The local non-volatile memory **245** may be, for example, PROM, EPROM, EEPROM, flash memory or traditional NVM. In a preferred embodiment, the local non-volatile memory **245** is EEPROM in order to optimally provide permanent reprogrammable data storage.

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